

DRAWING FOR PATENT APPLICATION
DARBY & DARBY P.C.
USPTO CUSTOMER NO: 38845
ATTORNEY DOCKET NO: 08211/0200375-US0
INVENTOR: Ha Chu Vu
TITLE: GENERATING ADJUSTABLE-DELAY CLOCK
SIGNAL FOR PROCESSING COLOR SIGNALS
1/14

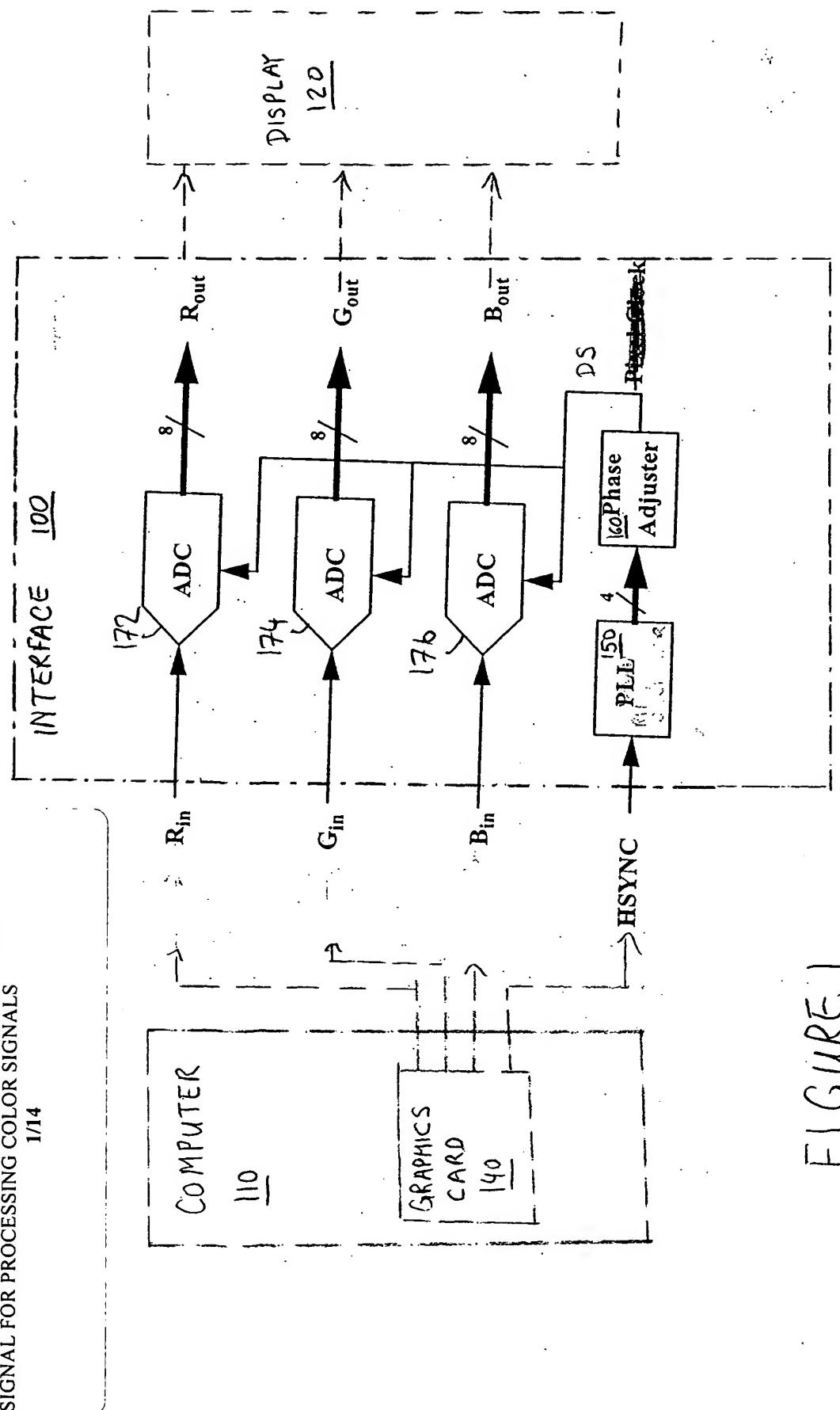


FIGURE 1

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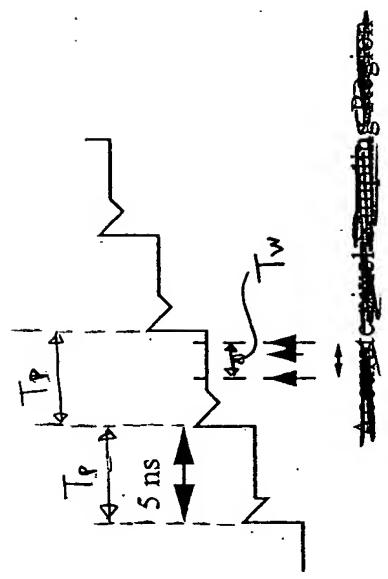


FIGURE 2

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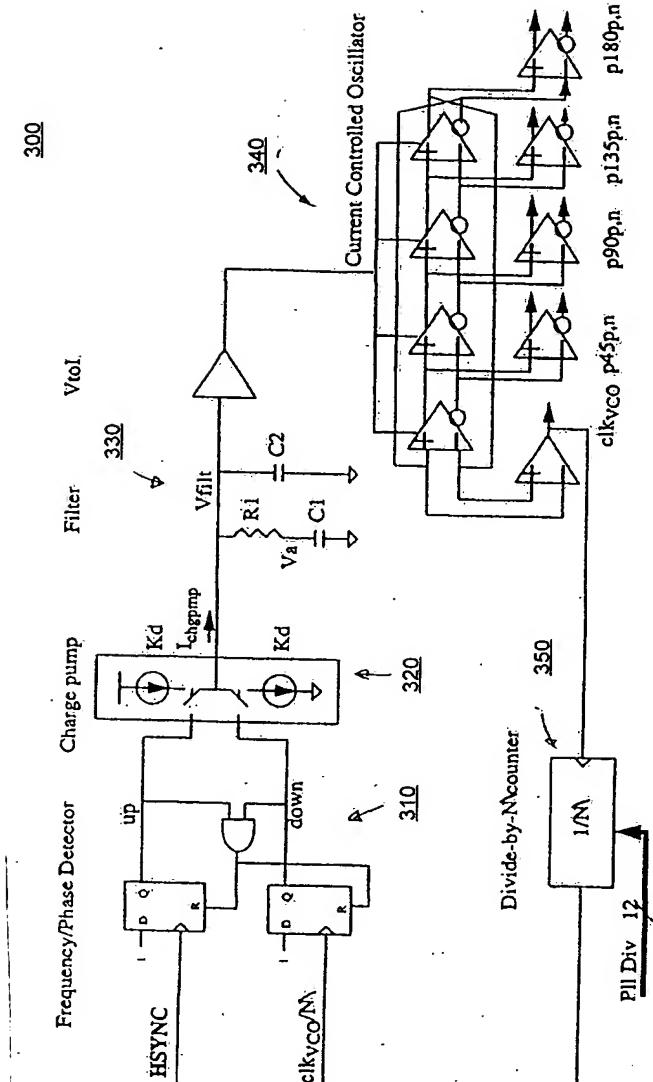


FIGURE 3

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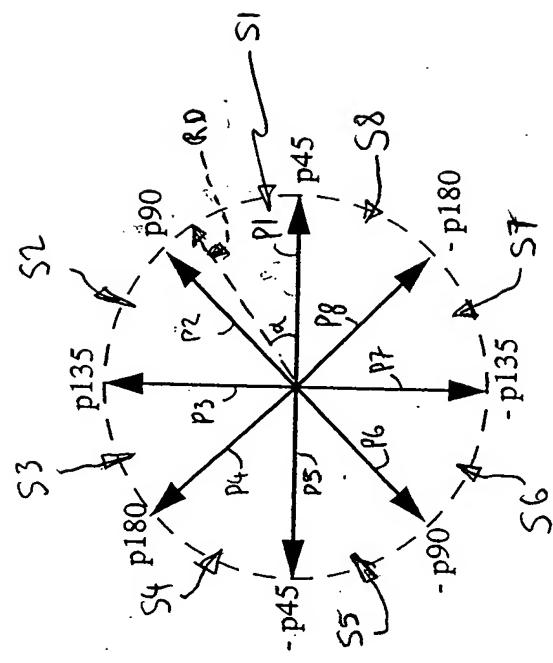


FIGURE 4

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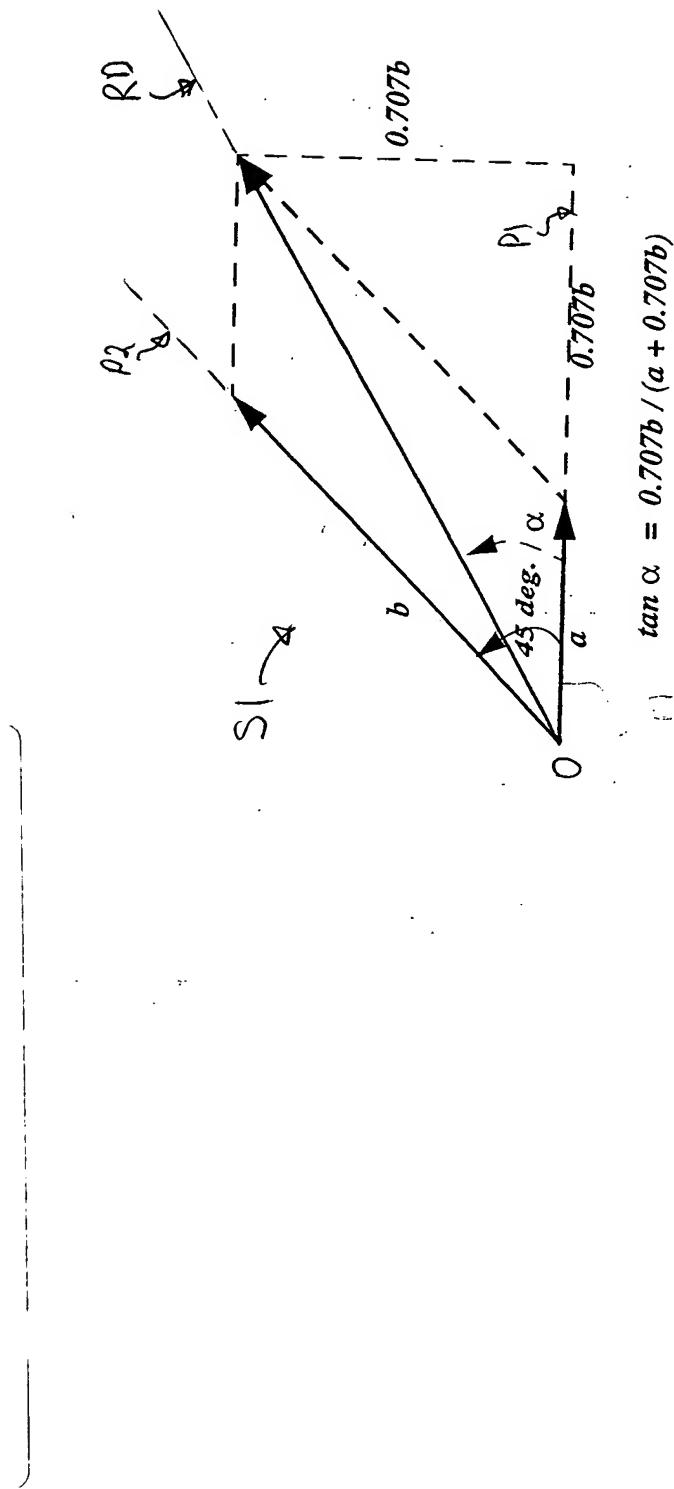
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ALUMNI RECORDS

INVENIENIOR: Hà Chu Vũ

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$$\tan \alpha = 0.707b / (a + 0.707b)$$

FIGURE 5

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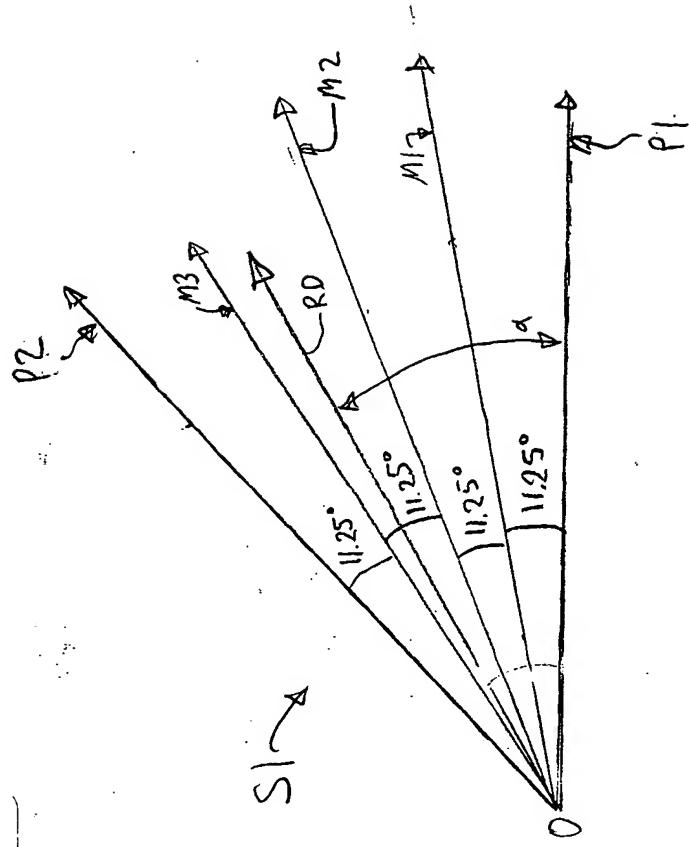


FIGURE 6

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700

SIMULATED SET 1

PHASE	α deg.	a	b	a	b
P1	0	16	0	4	0
M1	11.25	12	4	3	1
M2	22.5	8	8	2	2
M3	33.75	4	12	1	3
P2	45	0	16	0	4

TOTAL 1 = 16

TOTAL 2 = 4

Phase Step = $360 \text{ deg} / 32 = 11.25 \text{ deg}$

$$45 \text{ deg} / 4 = 11.25 \text{ deg}$$

WEIGHT VALUES FOR
SIMULATED PHASES

FIGURE 7

800

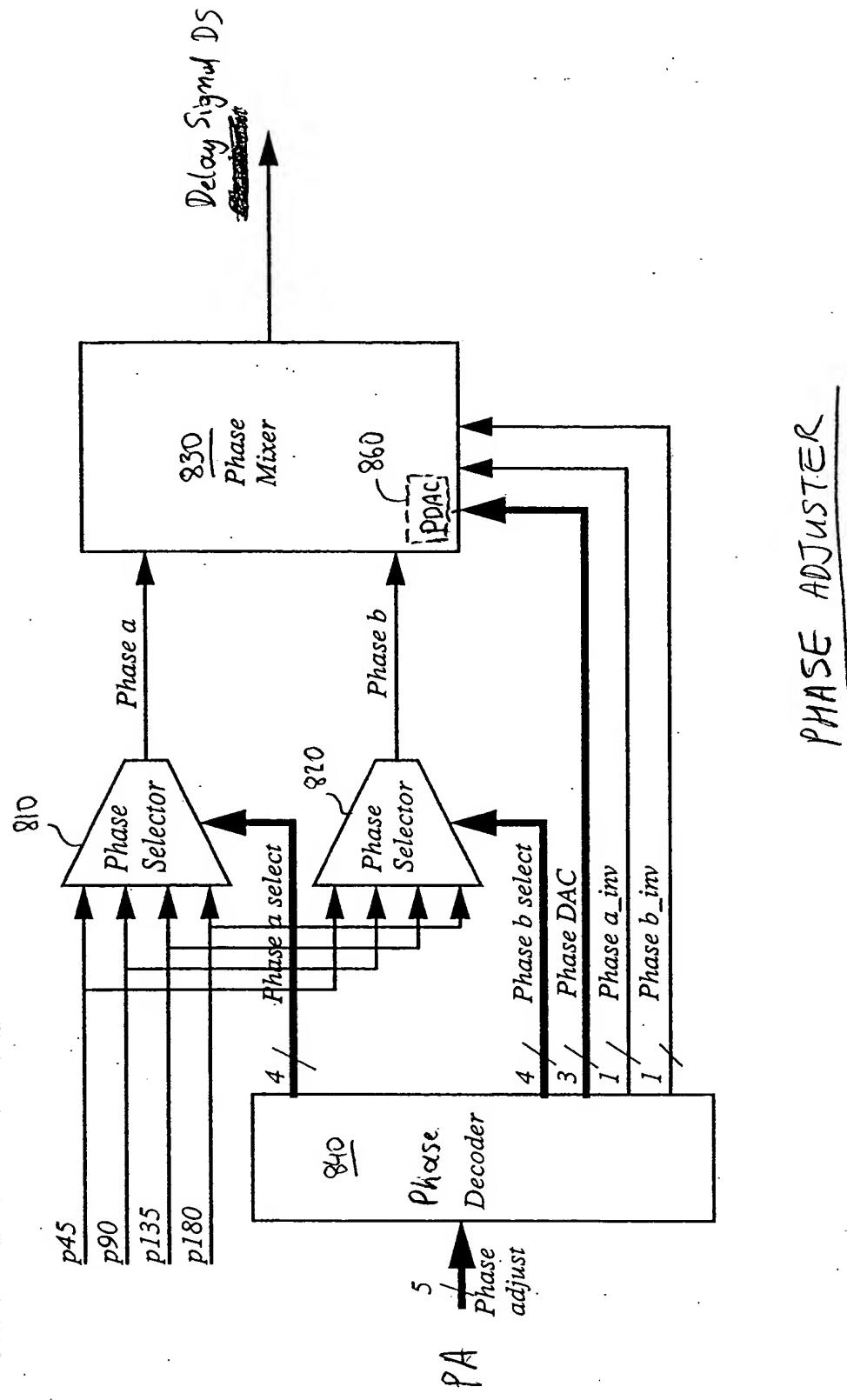


FIGURE 8

PHASE ADJUSTER

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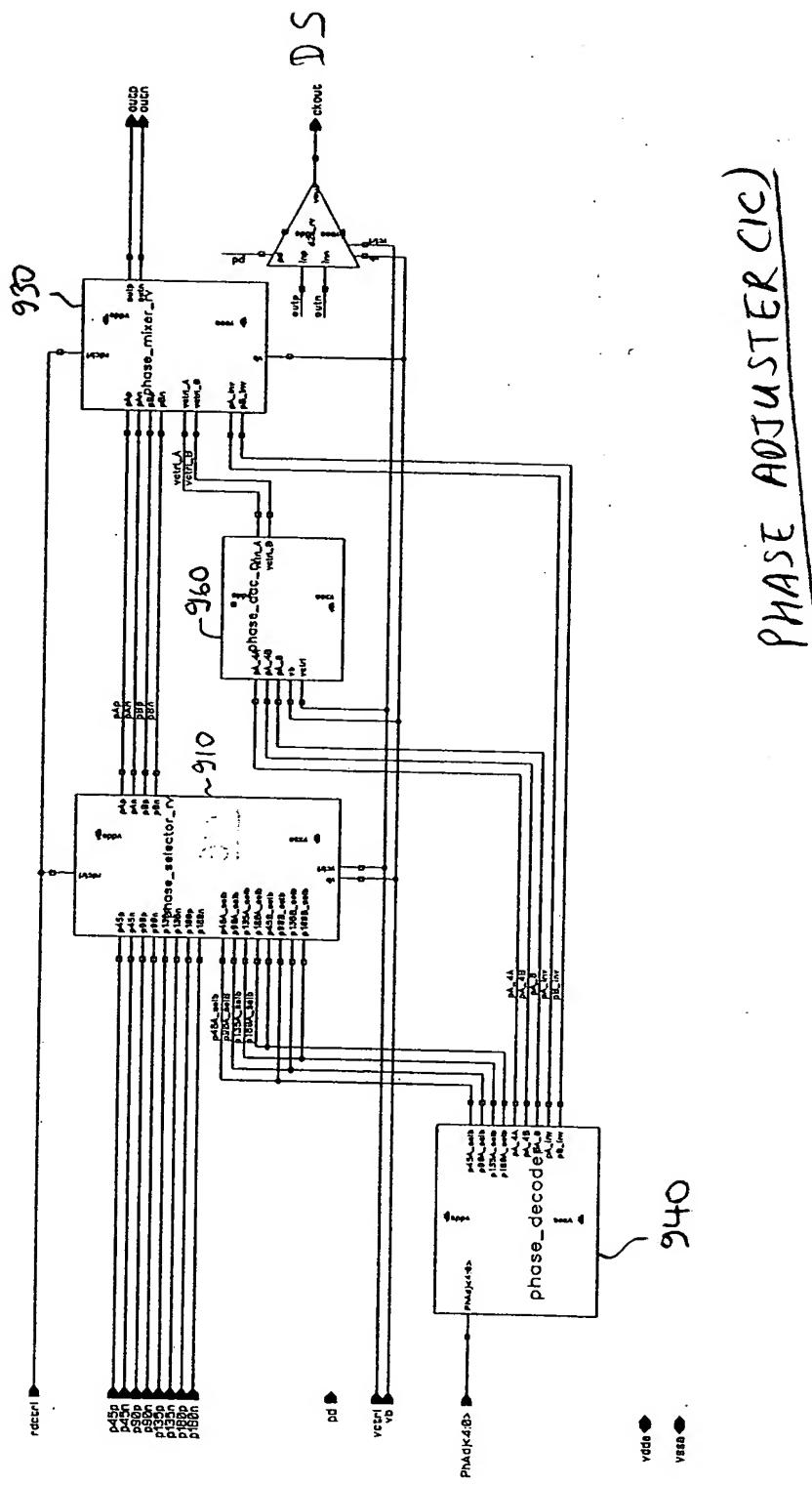
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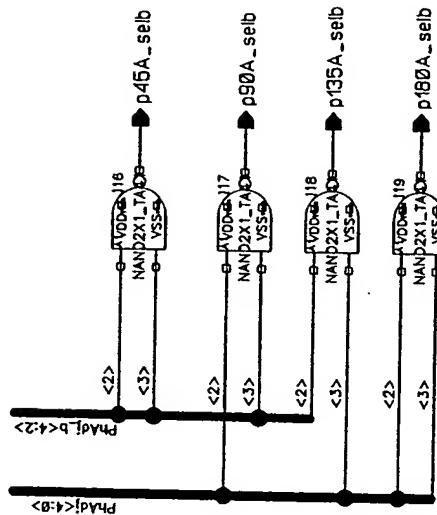
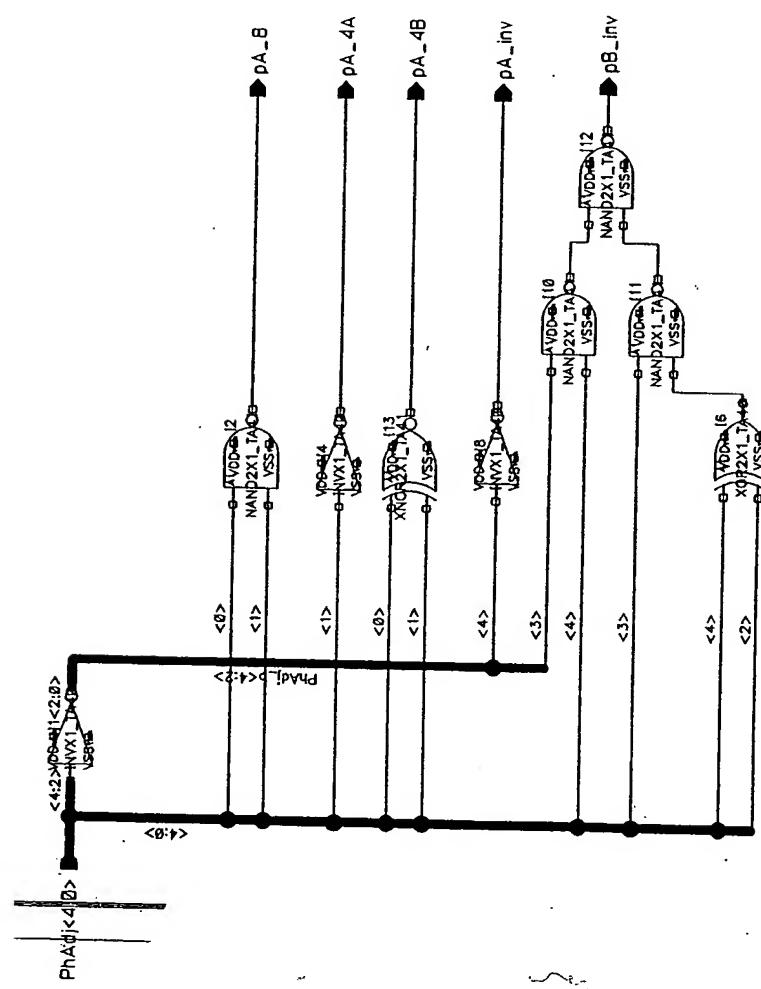
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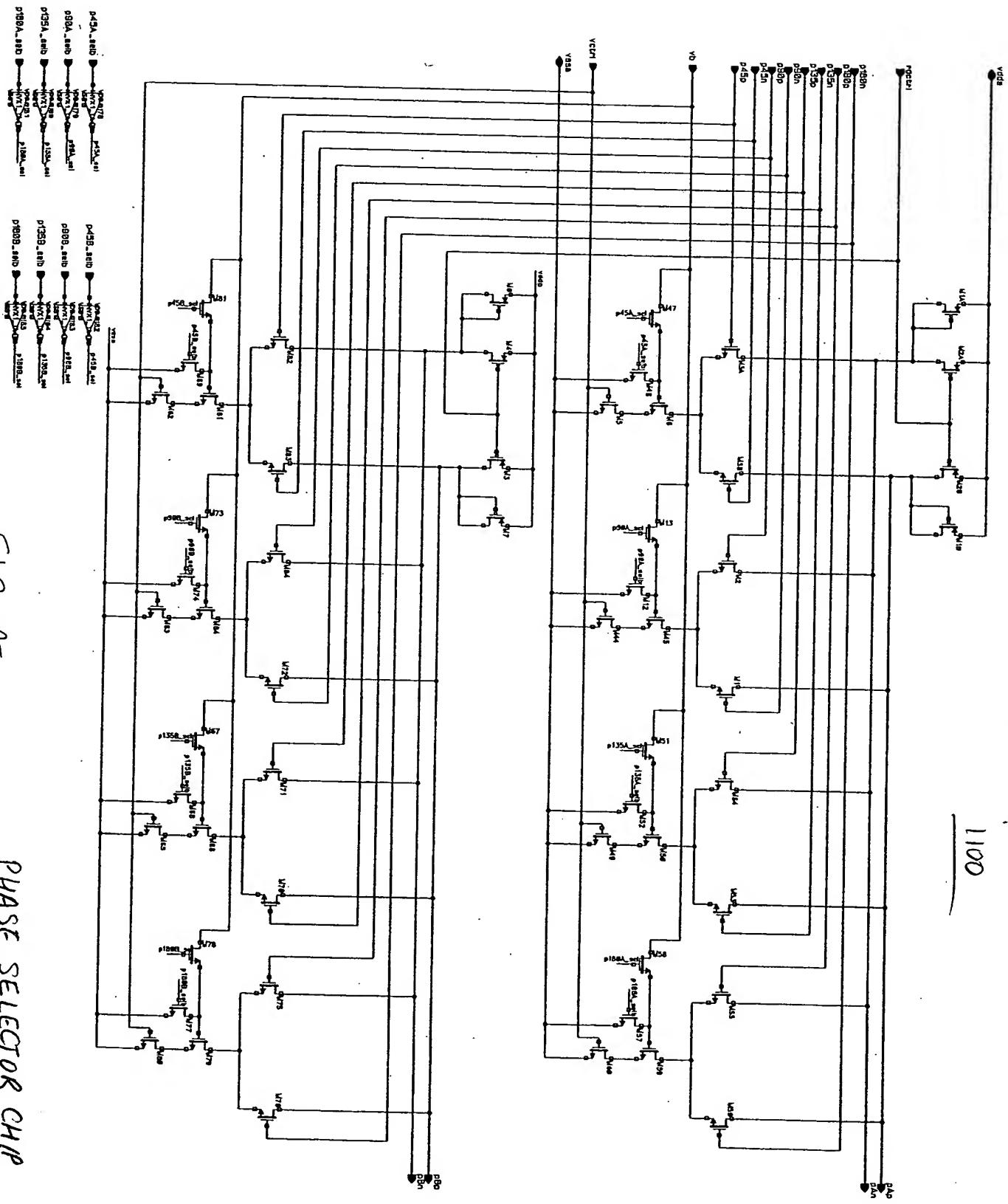
840



Phase Decoder

FIGURE 10

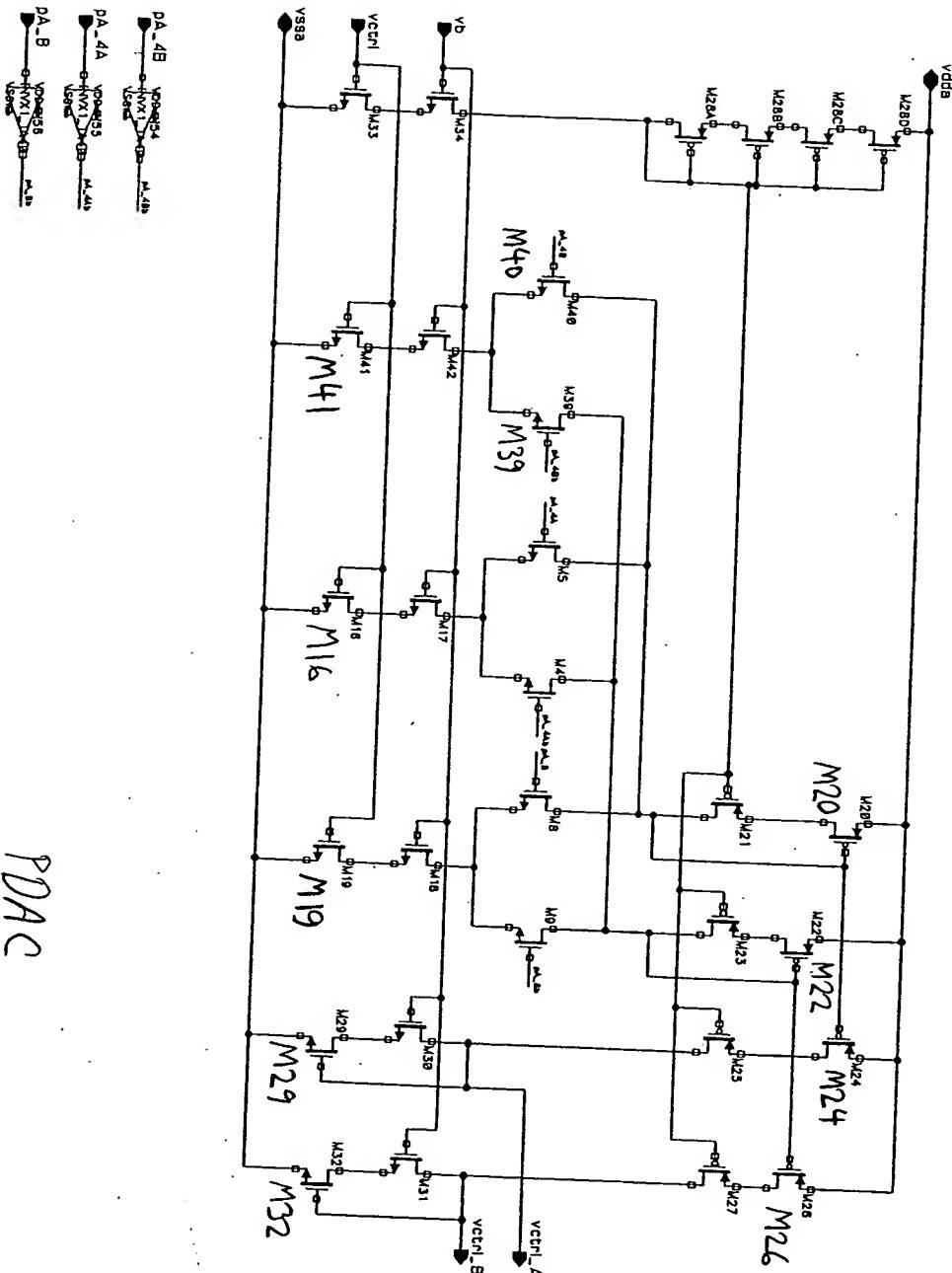
FIGURE 11
PHASE SELECTOR CHIP



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FIGURE 12

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SIGNAL FOR PROCESSING COLOR SIGNA

TITLE: GENERATING ADJUSTABLE DELAY CLOCK SIGNALS FOR PROCESSORS ON GRAINS

INVENTOR: Ha Chu Vu

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U.S. PATENT & TRADEMARK OFFICE

DARBY & DABBY PC
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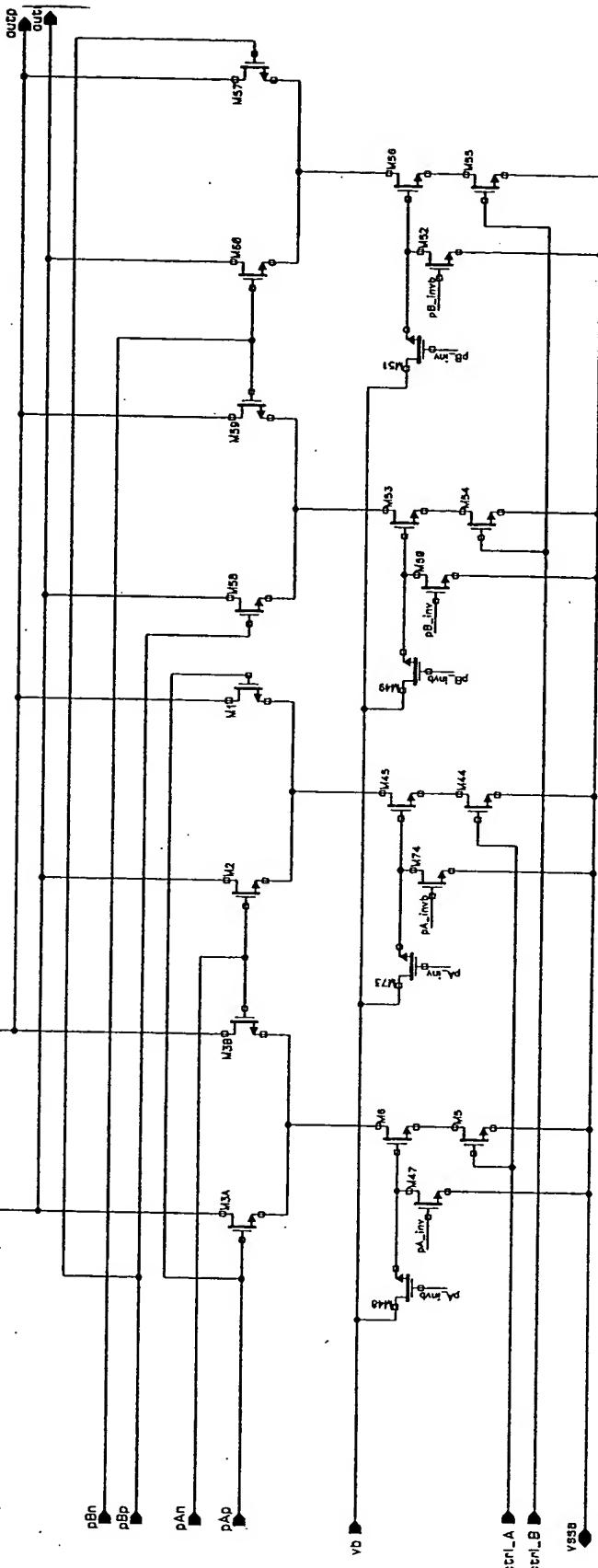
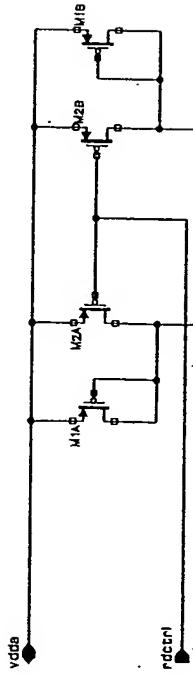
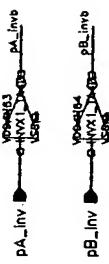
1300

FIGURE 13



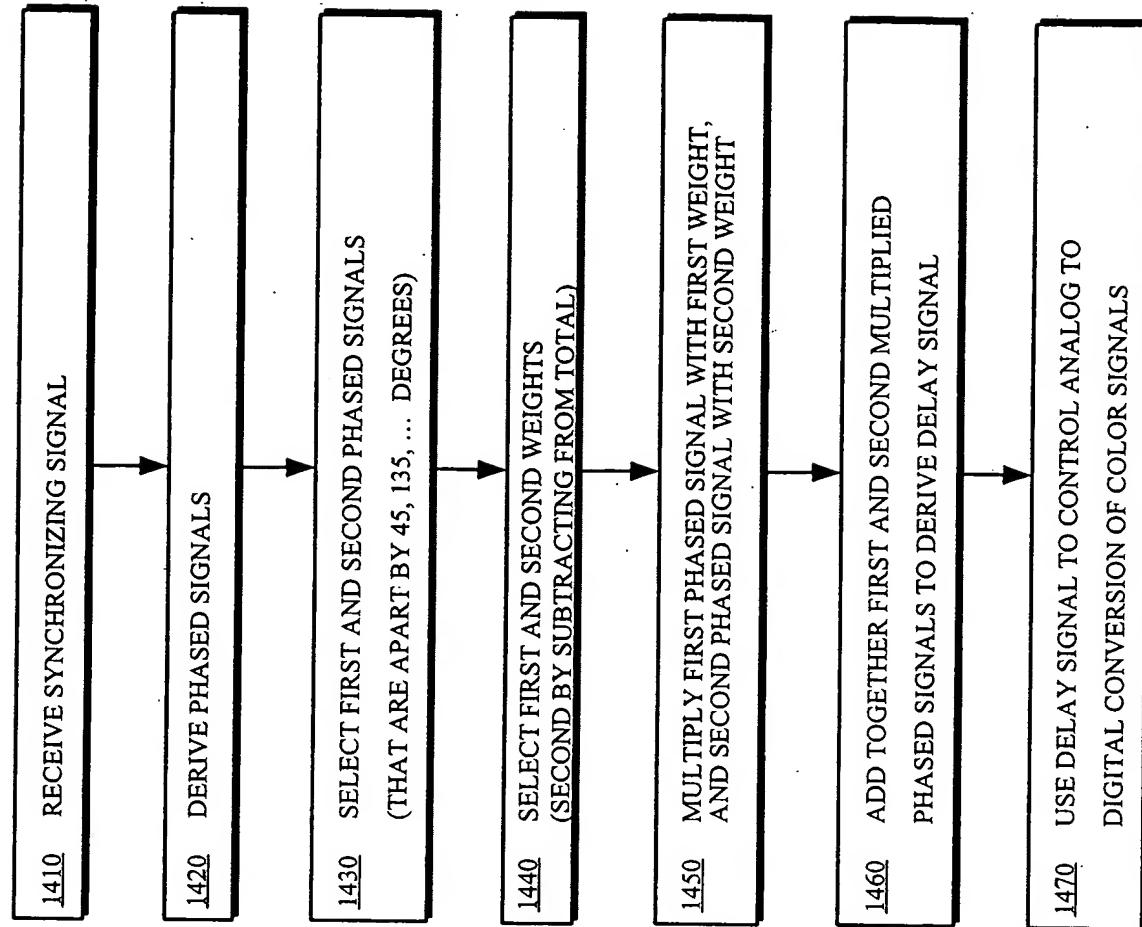


FIGURE 14